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TITLE: B3 HT215F01-100 **Product Specification For Qisda** Rev. A



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REVISION HISTORY

REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
0		Initial Release	2011.07.04	jiangweidong
Α		Frame Period 1149 lines→1251 lines	2011.07.06	jiangweidong

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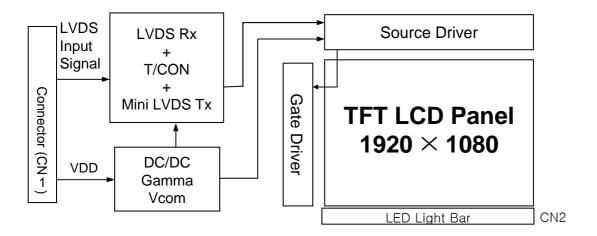
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1.0 GENERAL DESCRIPTION

1.1 Introduction

HT215F01-100 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 21.5 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- LVDS Interface with 2 pixel / clock
- High-speed response
- 6-bit (Hi-FRC) color depth, display 16. 7M colors
- Incorporated edge type back-light (One Light Bar)
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS /TCO 5.0 Compliant

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1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model HT215F01-100.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	476.64(H) × 268.11 (V)	mm	
Number of pixels	1920(H) ×1080(V)	pixels	
Pixel pitch	$0.24825(H) \times 0.24825(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally White		
Dimensional outline	$495.6(H) \times 292.2(V) \times 10.2(D)$ typ.	mm	
Weight	1450 (typ.)	g	
Surface Treatment	Haze 25%, 3H		
Back-light	Lower side 1-LED Light bar Type		

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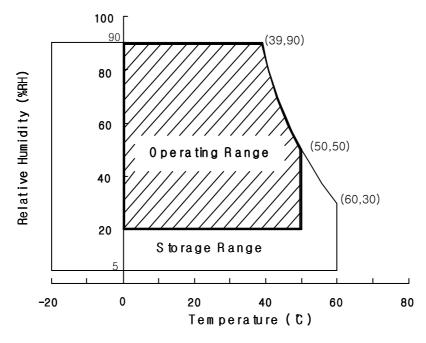
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> [VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.5	5.5	V	
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	V	$Ta = 25 \degree C$
Operating Temperature	T_{OP}	0	+50	$^{\circ}\!\mathbb{C}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}$ C	1)

Note : 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 $^{\rm O}{\rm C}$ max. and no condensation of water.





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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta =25 ± 2 °C]

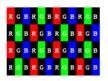
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	4.5	5.0	5.5	V	NI . 1
Power Supply Current	I _{DD}	-	700	1400	mA	Note1
In-Rush Current	I_{RUSH}	-	-	3	A	Note 2
Permissible Input Ripple Voltage	V _{RF}	-	-	100	mV	$V_{DD} = 5.0V$
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	-	-	mV	
Differential input voltage	V _{ID}	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		V _{IH} =100mV, V _{IL} =-100mV
LED Channel Voltage	V _L	-	54.4	59.5	V	
LED Channel Current	I_L	-	60	-	mA	
LED Lifetime		30,000	-	-	Hrs	I _L =60 mA
	P_{D}	-	3.5	-	W	
Power Consumption	P_{BL}	-	13.06	-	W	I _L =60 mA, Note 5
	P_{total}	-	16.56	-	W	

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz. Test Pattern of power supply current

a) Typ: Color Bar pattern
b) May: Skip Sub Pivel Patter

b) Max: Skip Sub Pixel Pattern



- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %
- 3. The lamp frequency should be selected as different as possible from the horizontal synchronous frequency and its harmonics to avoid interference, which may cause line flow on the display
- 4. The voltage above this value should be applied to the lamps for more than 1 second to start-up. Otherwise the lamps may not be turned on.
- 5. Calculated value for reference ($V_L \times I_L$) \times 4(channel) excluding driver loss. (LED Light bar: 17S4P)

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25 ± 2 °C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of Θ and Φ equal to 0°. We refer to $\Theta_{\emptyset=0}$ (= Θ_3) as the 3 o'clock direction (the "right"), $\Theta_{\emptyset=90}$ (= Θ_{12}) as the 12 o'clock direction ("upward"), $\Theta_{\emptyset=180}$ (= Θ_9) as the 9 o'clock direction ("left") and $\Theta_{\emptyset=270}$ (= Θ_6) as the 6 o'clock direction ("bottom"). While scanning Θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 78MHz, I_{BI} = 7.5mA, Ta =25 ± 2 °C]

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	TT ' . 1	Θ_3		70	85	-	Deg.	
	Horizontal	Θ_9	CR > 10	70	85	-	Deg.]
Viewing Angle range	Vertical	Θ_{12}	CR > 10	70	80	-	Deg.]
	vertical	Θ_6		70	80	-	Deg.	Note 1
	Horizontal	Θ_3		85	-	-	Deg.	Note 1
Viewing Angle range	Horizontai	Θ_9	CR > 5	85	-	-	Deg.	
Viewing Angle range	Vertical	Θ_{12}	CK > 3	85	-	-	Deg.	
	Vertical	Θ_6		85	-	-	Deg.	1
Luminance Contrast r	atio	CR		700	1000			Note 2
Luminance of White		Y_{w}		200	250		cd/m ²	Note 3
White luminance unif	ormity	ΔΥ		75	80		%	Note 4
	White	$\mathbf{W}_{\mathbf{x}}$		0.283	0.313	0.343		
	White	\mathbf{W}_{y}	$\Theta = 0^{\circ}$ (Center)	0.299	0.329	0.359]
	Red	R_x	Normal	0.609	0.639	0.669]
Reproduction	Keu	R_y	Viewing Angle	0.312	0.342	0.372		Note 5
of color	Green	G_x	C	0.293	0.323	0.353		11000 5
	Green	G_y		0.600	0.630	0.660		
	Blue	B_x		0.123	0.153	0.183]
	Diuc	B_y		0.021	0.051	0.081		
Response	Rising	$T_{\rm r}$			1.5	2.5	ms	Note 6
Time	Falling	T_{f}			3.5	5.5	ms	11010 0
Cross Ta	lk	CT		-	-	2.0	%	Note 7

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Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 9points / Maximum Luminance of 9points) * 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

● CN1 Module Side Connector : UJU IS100-L300-C23 or Equivalent User Side Connector : JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	RXO0-	Negative Transmission data of Pixel 0 (ODD)	
2	RXO0+	Positive Transmission data of Pixel 0 (ODD)	
3	RXO1-	Negative Transmission data of Pixel 1 (ODD)	
4	RXO1+	Positive Transmission data of Pixel 1 (ODD)	
5	RXO2-	Negative Transmission data of Pixel 2 (ODD)	
6	RXO2+	Positive Transmission data of Pixel 2 (ODD)	
7	GND	Power Ground	
8	RXOC-	Negative Transmission Clock (ODD)	
9	RXOC+	Positive Transmission Clock (ODD)	
10	RXO3-	Negative Transmission data of Pixel 3 (ODD)	
11	RXO3+	PositiveTransmission data of Pixel 3 (ODD)	
12	RXE0-	Negative Transmission data of Pixel 0 (EVEN)	
13	RXE0+	Positive Transmission data of Pixel 0 (EVEN)	
14	GND	Power Ground	
15	RXE1-	Negative Transmission data of Pixel 1 (EVEN)	
16	RXE1+	Positive Transmission data of Pixel 1 (EVEN)	
17	GND	Power Ground	
18	RXE2-	Negative Transmission data of Pixel 2 (EVEN)	
19	RXE2+	Positive Transmission data of Pixel 2 (EVEN)	
20	RXEC-	Negative Transmission Clock (EVEN)	
21	RXEC+	Positive Transmission Clock (EVEN)	
22	RXE3-	Negative Transmission data of Pixel 3 (EVEN)	
23	RXE3+	Positive Transmission data of Pixel 3 (EVEN)	
24	GND	Power Ground	Note1
25	NC	Not connection, this pin should be open	
26	NC	Not connection, this pin should be open	
27	NC	Not connection	
28	VDD1	Power Supply:+5V	
29	VDD2		
30	VDD3		

Note 1: This pin should be connected with GND

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5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent) 5.2.1 ODD LVDS Interface

	Input	Trans	mitter	Inter	face	HT215F01-100 (CN11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	OR0	51					
	OR1	52					
	OR2	54	48	OUT0-	RXO0-	1	
	OR3	55	48 47	OUT0+	RXO0+	1 2	
	OR4	56	.,	00101	Tu 1001	_	
	OR5	3					
	OG0	4					
	OG1	6					
	OG2	7					
	OG3	11	46	OUT1-	RXO1-	2	
	OG4	12	46 45	OUT1+	RXO1+	3 4	
	OG5	14				'	
О	OB0	15					
D D	OB1	19					
"	OB2	20					
L	OB3	22					
V	OB4	23	40	OLUTO	DWG	_	
D	OB5	24	42 41	OUT2- OUT2+	RXO2- RXO2+	5 6	
S	Hsync	27	41	0012+	KAO21	O O	
	Vsync	28					
	DE	30					
	MCLK	31	40 39	CLK OUT- CLK OUT+	RXO CLK- RXO CLK+	8 9	
	OR6	50				-	
	OR7	2					
	OG6	8			RXO3-		
	OG7	10	38	OUT3-	RXO3+	10	
	OB6	16	37	OUT3+		11	
	OB7	18					
	RSVD	25					

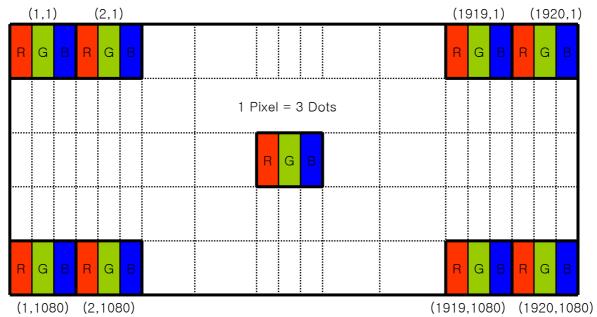
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5.2.2 EVEN LVDS Interface

	Input	Trans	mitter	Inter	face	HT215F01-100 (C N11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	ER0	51					
	ER1	52					
	ER2	54	40	OUT0-	RXE0-	12	
	ER3	55	48 47	OUT0+	RXE0+	12 13	
	ER4	56	.,	00101	TO LEG 1	13	
	ER5	3					
	EG0	4					
	EG1	6					
	EG2	7					
	EG3	11	4.6	OLUM1	DVE1	1.5	
	EG4	12	46 45	OUT1- OUT1+	RXE1- RXE1+	15 16	
	EG5	14	13			10	
E V	EB0	15					
Ė	EB1	19					
N	EB2	20					
_	EB3	22					
L V	EB4	23		O.V.T.	DATES	4.0	
D	EB5	24	42 41	OUT2- OUT2+	RXE2- RXE2+	18 19	
S	Hsync	27	71	0012+	KAL2+	19	
	Vsync	28					
	DE	30					
	MCLK	31	40	CLK OUT-	RXE CLK-	20	
			39	CLK OUT+	RXE CLK+	21	
	ER6	50					
	ER7	2					
	EG6	8	38	OUT3-	RXE3-	22	
	EG7	10	37	OUT3+	RXE3+	23	
	EB6	16					
	EB7	18					
	RSVD	25					

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5.3 Data Input Format



Display Position of Input Data (V-H)

5.4 Back-light Interface Connection

●CN 2 LED LightBar Connector:CI1406M1HRL-NH or equivalent

Pin	Function
1	Channel 1 Current Feedback
2	Channel 2 Current Feedback
3	LED Power Supply
4	LED Power Supply
5	Channel3 Current Feedback
6	Channel4 Current Feedback

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6.0 SIGNAL TIMING SPECIFICATION

6.1 The HT215F01-100 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	57.5	74.32	94	MHz
Clock	High Time	Tch	-	4/7 Tc	-	
	Low Time		-	3/7 Tc	-	
·			1091	1125	1251	lines
Fı	rame Period	Tv	50	60	75	Hz
			20	16.67	13.33	ms
Vertical Display Period		Tvd	1	1080	1	lines
One line Scanning Period		Th	1060	1100	1251	clocks
Horizon	tal Display Period	Thd	-	960	-	clocks

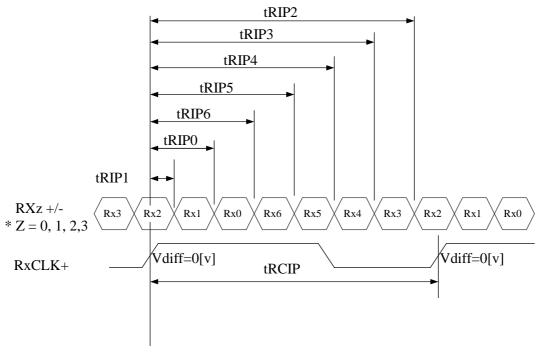
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6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.76	13.46	16.15	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	$2 \times tRCIP/7 + 0.4$	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	$3 \times tRCIP/7 + 0.4$	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	$4 \times tRCIP/7 + 0.4$	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 ×tRCIP/7	5 ×tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	6 ×tRCIP/7-0.4	6 ×tRCIP/7	6 ×tRCIP/7+0.4	nsec	

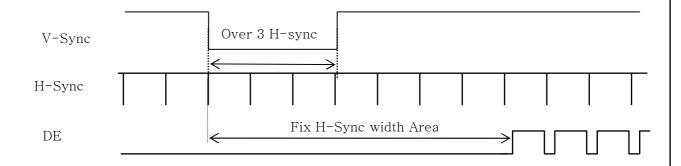


* Vdiff = (RXz+)-(RXz-),....,(RXCLK+)-(RXCLK-)

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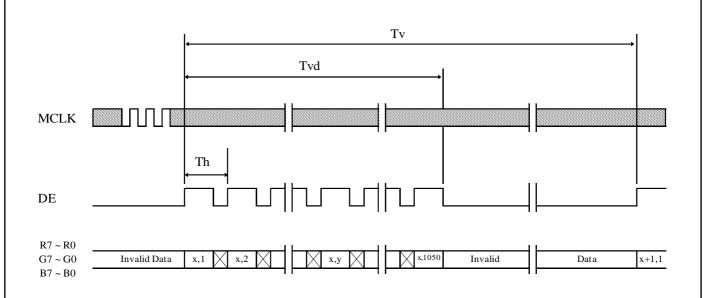
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms



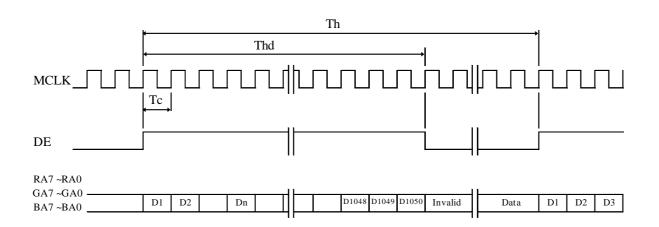
- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

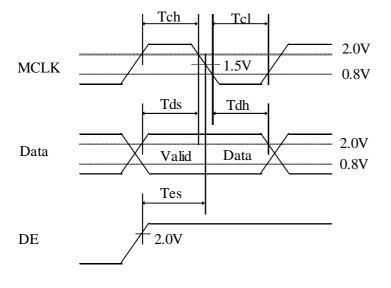
7.2 Vertical Timing Waveforms



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7.3 Horizontal Timing Waveforms





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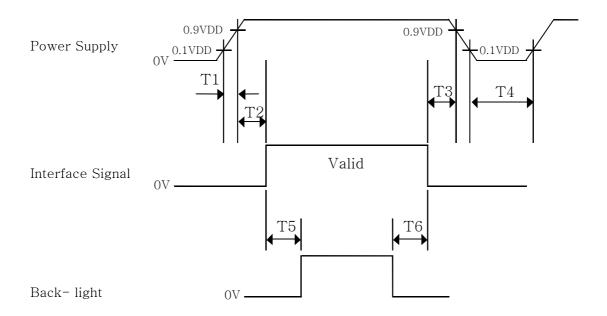
8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

G 1 0 6				RF	ED I	DA7	ГΑ				(GRI	EEN	I DA	ΛΤΑ					BL	UE	DA	TA		
Color & C	ray Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
D : C 1	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Δ													1											
of RED	∇				,	,							,	Į							,	ļ			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	\triangle				1	<u> </u>							′	1							′	<u> </u>			
of GREEN	∇				,	,							,	Į.							,	ļ			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	\triangle				1	`							1	1							1	Ì			
OLDLUE	∇				,	ļ							,	ļ							,	l			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
I	Δ													<u> </u>											
of WHITE	∇					ļ																l			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- $0.5 \text{ ms} \le T1 \le 10 \text{ ms}$
- \bullet 0 \leq T2 \leq 50 ms
- \bullet 0 \leq T3 \leq 50 ms
- \bullet 1 sec \leq T4
- \bullet 200 ms \leq T5
- \bullet 200 ms \leq T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HT215F01-100. Other parameters are shown in Table 5.

< Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	495.6 ×292.2×10.2	mm
Weight	1450 (typ.)	gram
Active area	476.64(H) × 268.11(V)	mm
Pixel pitch	0.24825(H) x 0.24825(V)	mm
Number of pixels	$1920(H) \times 1080(V)$ (1 pixel = R + G + B dots)	pixels
Back-light	Lower side 1-LED Light bar Type	

10.2 Mounting

See FIGURE 5. (shown in Appendix)

10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

No	Test Items	Conditions				
1	High temperature storage test	$Ta = 60 ^{\circ}\text{C}$, 240 hrs				
2	Low temperature storage test	Ta = -20 °C, 240 hrs				
3	High temperature & high humidity operation test	$Ta = 50 ^{\circ}\text{C}, 80\% \text{F}$	RH, 240hrs			
4	High temperature operation test	$Ta = 50 ^{\circ}\text{C}$, 240h	rs			
5	Low temperature operation test	$Ta = 0 ^{\circ}C$, 240hrs	1			
6	Thermal shock	$Ta = -20 \ ^{\circ}\text{C} \leftrightarrow 60$) $^{\circ}$ C (0.5 hr), 100 cycle			
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	10 ~ 300 Hz, Sweep rate 30 min 1.5 G X, Y, Z 30 min			
		Gravity	50G			
8	Shock test (non-operating)	Pulse width	11msec, sine wave			
		Direction	\pm X, \pm Y, \pm Z Once for each			
9	Electro-static discharge test (non-operating)	Air : 150 pF , 330Ω , 15 KV Contact : 150 pF , 330Ω , 8 KV				
10	Altitude test	Operating: 0 to 16400ft, 0 to 40° Non Operating: 0 to 40000ft, -20 to 40°				

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12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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13.0 PRODUCT SERIAL NUMBER



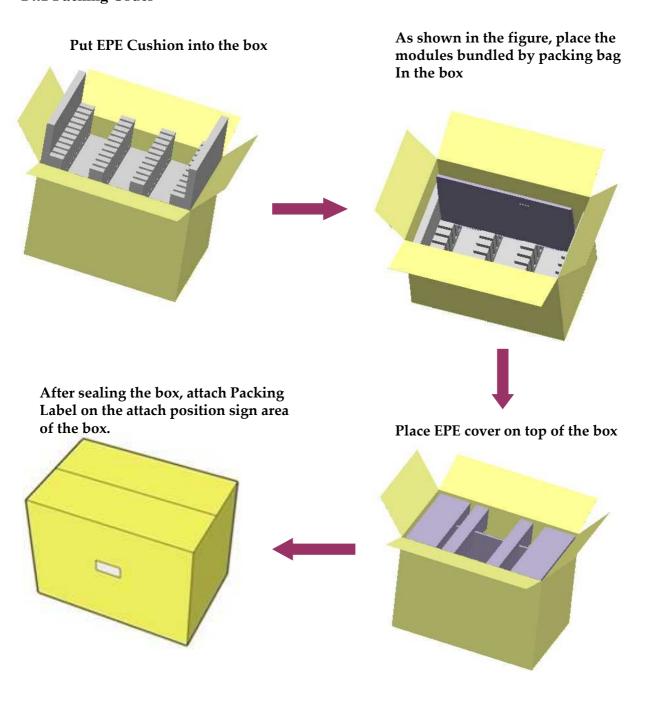
- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001:01, 2002:02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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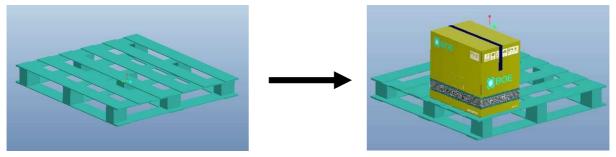
14.0 Packing

14.1 Packing Order

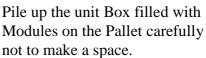


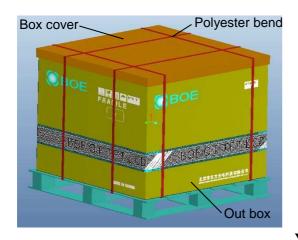
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14.2 Pallet Packing Order



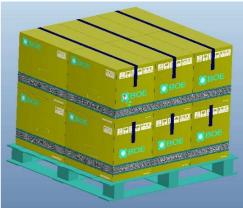
Load Pallet in correct position





Adjust Outer box to piling Boxes and cover the Outer box using box cover. Bend Pallet Assy which is putting the Box Cover tight in two direction (horizontal and vertical) and two times using Polyester bend.





Wrap the Pallet Assy loaded 1story, 2stories from downside through side and to edge of upside. (12Boxes/Pallet)

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14.3 Packing Specification and Note

		Specification			
Item	Q'ty	Dimension	Weight (kg)	Remark	
Panel	1	$495.6(H) \times 292.2(V) \times 10.2(D)$ typ.	1.48	-	
Cushion	1	-	0.70	-	
Box	1	558(L)mm x 348(W)mm x 400(H)mm	0.70	without Panel & cushion	
Packing Box	10pcs/Bo x	558(L)mm x 348W)mm x 400(H)mm	16.20	with panel & cushion	
Pallet	1	1200(L)mm x 1140(W)mm x 120(H)m m	15.90	-	
Pallet after Packing	12boxes/ pallet	1140(L)mm x 1076W)mm x 820(H)mm	214	-	

14.3 Box label

• Label Size : $108 \text{ mm (L)} \times 56 \text{ mm (W)}$

• Contents

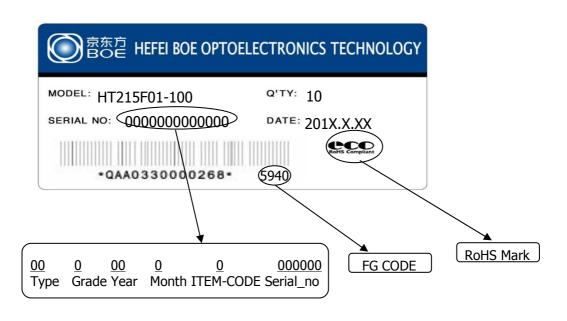
Model: HT215F01-100

Q`ty: Module Q`ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date

FG Code: FG Code of Product



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15.0 APPENDIX

Figure 1. Measurement Set Up

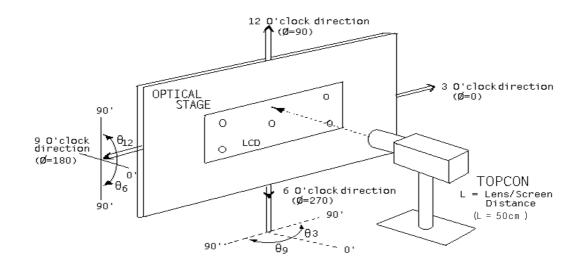
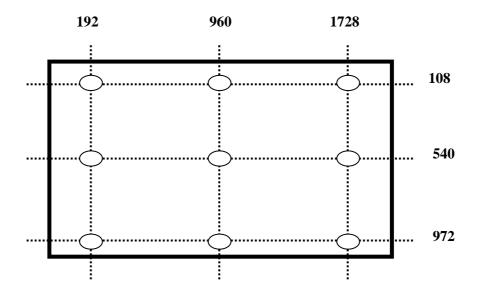


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



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Figure 3. Response Time Testing

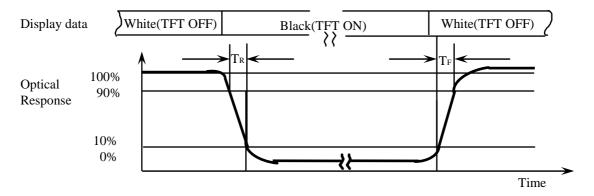
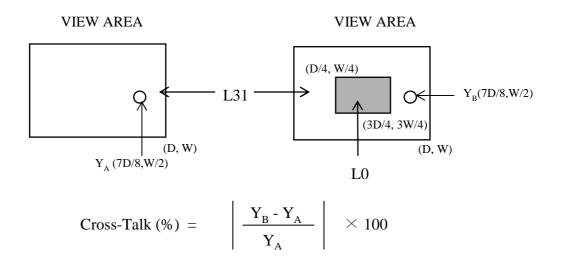


Figure 4. Cross Modulation Test Description



 Y_A = Initial luminance of measured area (cd/m²) Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

A4(210 X 297) R2010-6053-O(3/3)

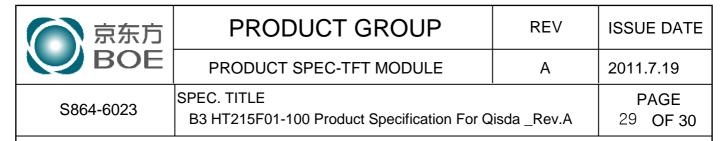
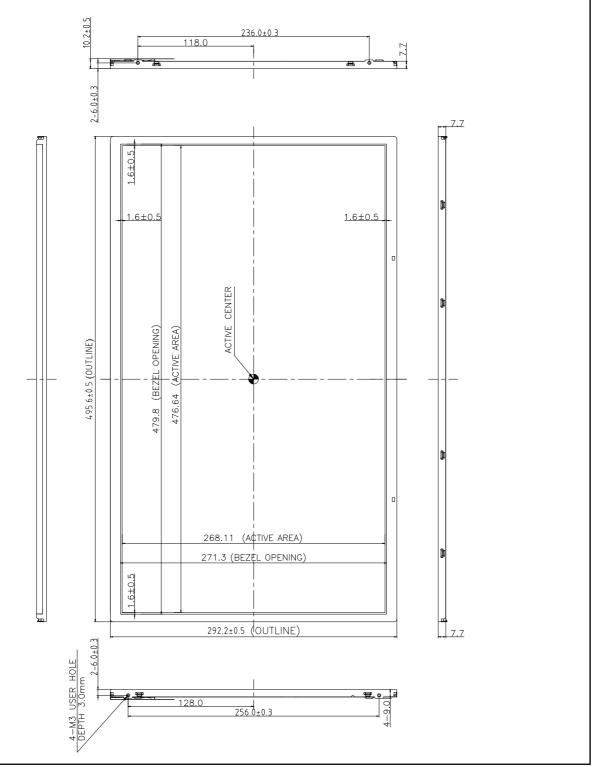


Figure 5. TFT-LCD Module Outline Dimensions (Front view)



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